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PATENT NUMBER

17

## U.S. UTILITY Patent Application

**O.I.P.E.**

**PATENT DATE**

**SCAND**

APPLICATION NO.  
09/801209

CONT/PRIOR  
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**CLASS**  
257

**SUBCLASS**  
221

ART UNIT  
2817

**EXAMINER**

**TITLE . . . APPLICANTS**

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(Divisional PCT)

Ferroelectric transistor; Use thereof in a memory cell configuration and method of producing the ferroelectric transistor

PTO-2040  
1289

**ISSUING CLASSIFICATION**

[illegible]

|  |   |             |            |                                   |                      |
|--|---|-------------|------------|-----------------------------------|----------------------|
| <input checked="" type="checkbox"/> <b>TERMINAL DISCLAIMER</b>   | <b>DRAWINGS</b>                                 |             |            | <b>CLAIMS ALLOWED</b>             |                      |
|  | Sheets Drwg.                                    | Figs. Drwg. | Print Fig. | Total Claims                      | Print Claims or O.G. |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.                                 | _____ (Assistant Examiner) _____ (Date)         |             |            | <b>NOTICE OF ALLOWANCE MAILED</b> |                      |
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